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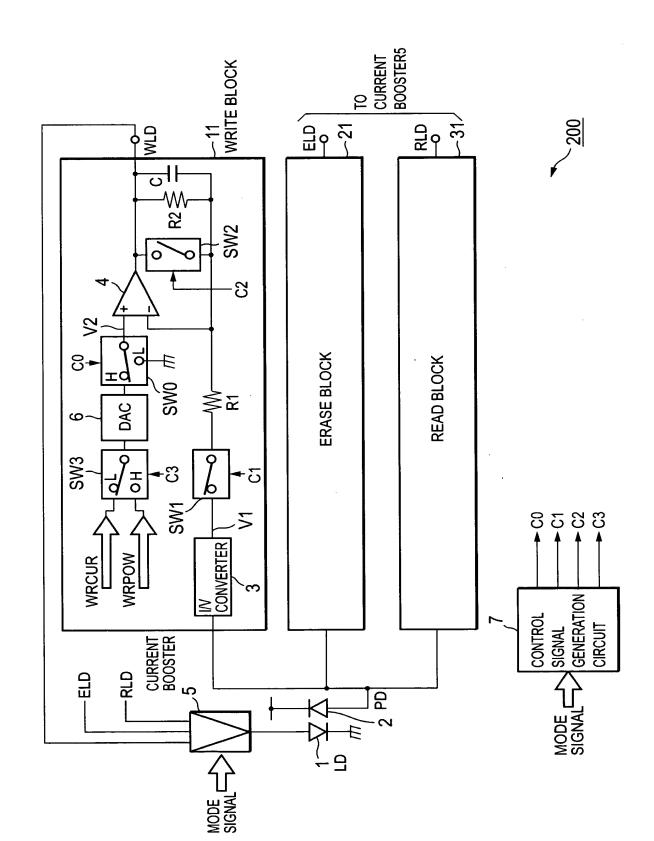
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FIG.1



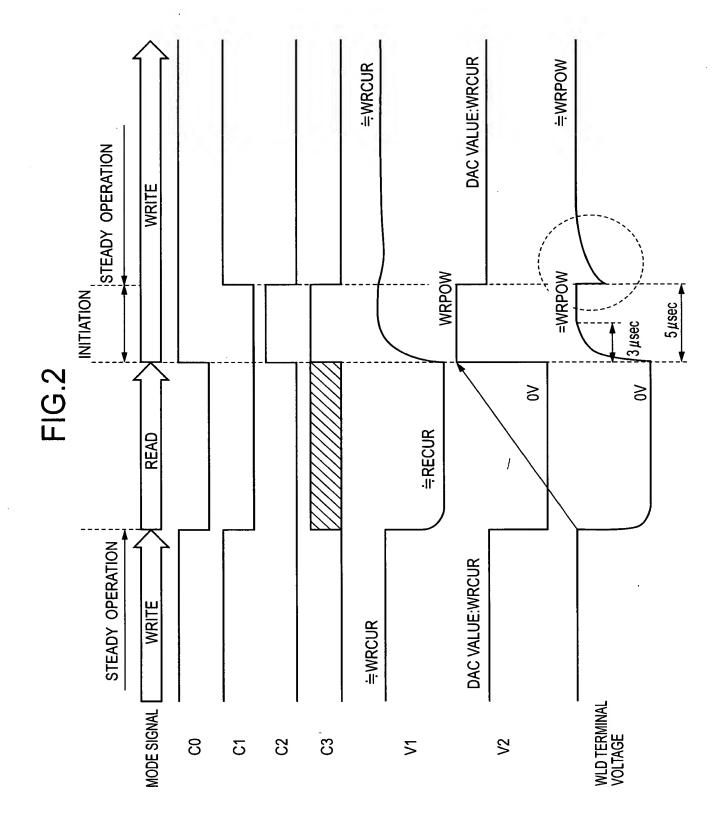
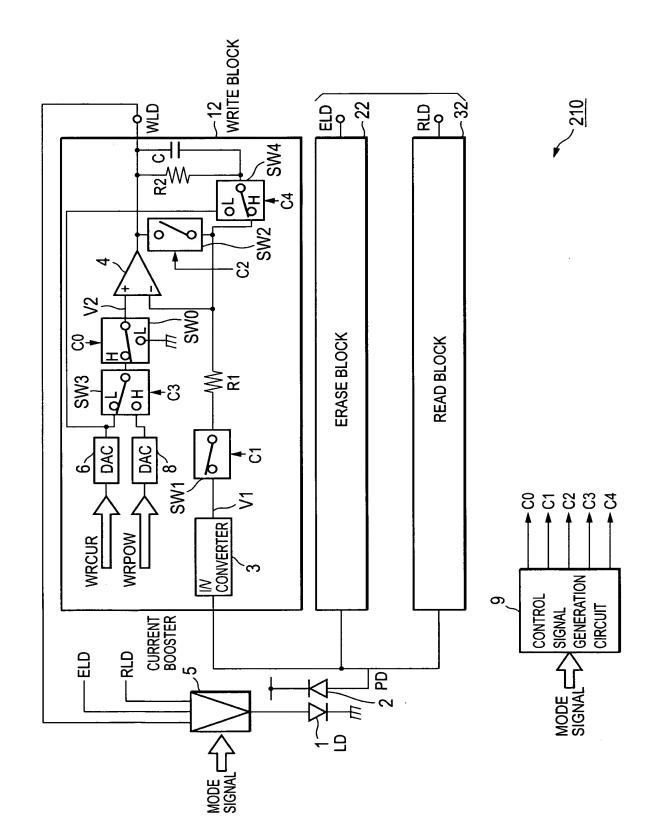


FIG.3



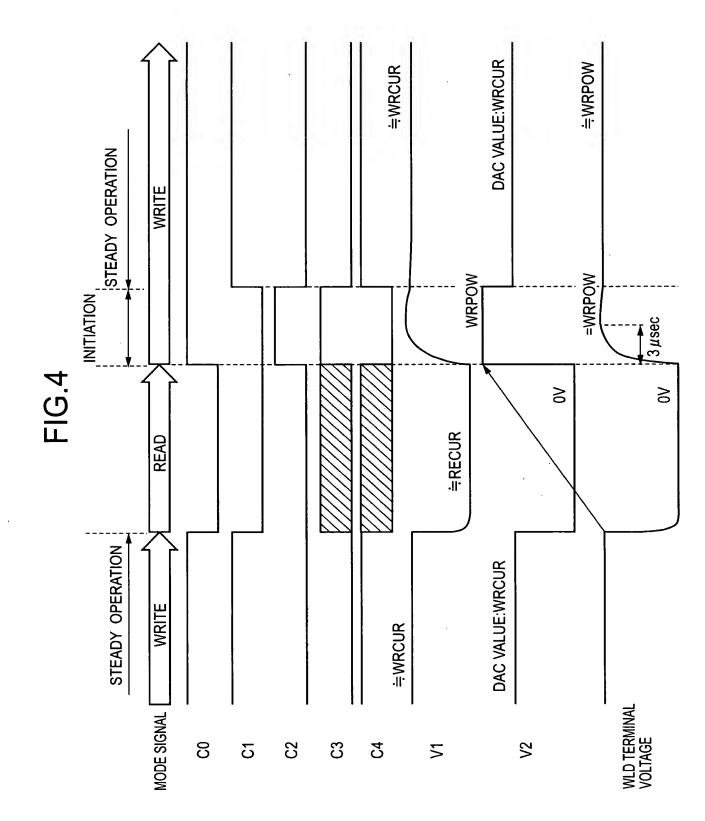


FIG.5

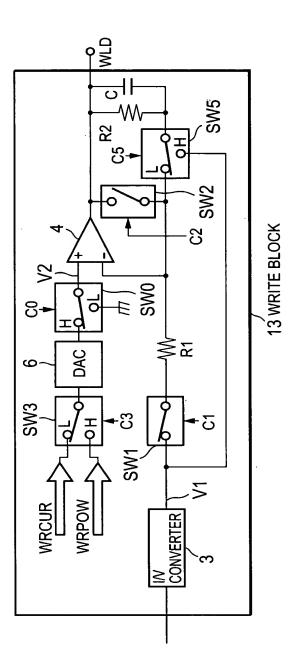


FIG.6

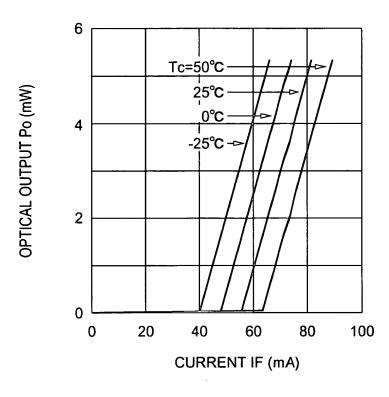


FIG.7 PRIOR ART

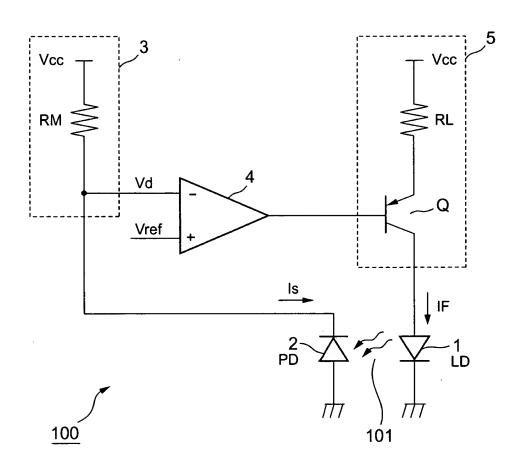
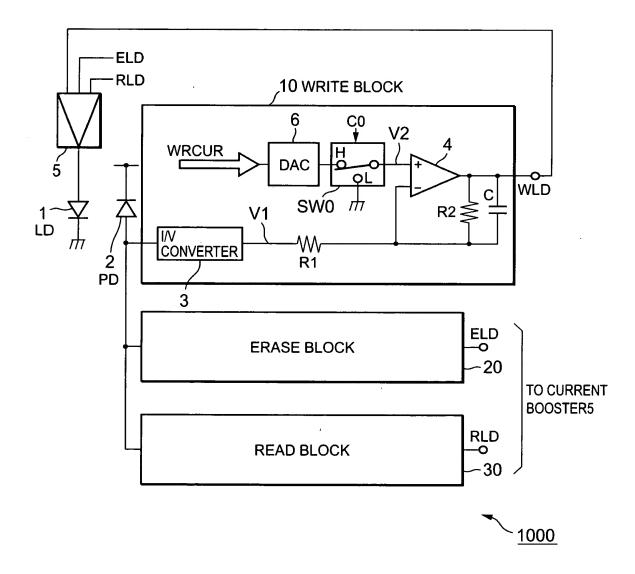


FIG.8 PRIOR ART



DAC VALUE:WRCUR **≑WRCUR** WRITE TENS OF µsec,OR MORE FIG.9 PRIOR ART WRPOW ÷RECUR READ 8 DAC VALUE:WRCUR WRITE ÷WRCUR WLD TERMINAL VOLTAGE MODE SIGNAL ප > 72